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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/690,655	10/21/2003	Steven C. Ciccarelli	020548	5866

23696 7590 11/03/2005

QUALCOMM, INC
5775 MOREHOUSE DR.
SAN DIEGO, CA 92121

EXAMINER

NGUYEN, THUAN T

ART UNIT	PAPER NUMBER
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2685

DATE MAILED: 11/03/2005

Please find below and/or attached an Office communication concerning this application or proceeding.

Office Action Summary

Application No.

10/690,655

Applicant(s)

CICCARELLI

Examiner

THUAN T. NGUYEN

Art Unit

2685

-- The MAILING DATE of this communication appears on the cover sheet with the correspondence address --

Period for Reply

A SHORTENED STATUTORY PERIOD FOR REPLY IS SET TO EXPIRE 3 MONTH(S) FROM THE MAILING DATE OF THIS COMMUNICATION.

- Extensions of time may be available under the provisions of 37 CFR 1.136(a). In no event, however, may a reply be timely filed after SIX (6) MONTHS from the mailing date of this communication.
- If the period for reply specified above is less than thirty (30) days, a reply within the statutory minimum of thirty (30) days will be considered timely.
- If NO period for reply is specified above, the maximum statutory period will apply and will expire SIX (6) MONTHS from the mailing date of this communication.
- Failure to reply within the set or extended period for reply will, by statute, cause the application to become ABANDONED (35 U.S.C. § 133). Any reply received by the Office later than three months after the mailing date of this communication, even if timely filed, may reduce any earned patent term adjustment. See 37 CFR 1.704(b).

Status

- 1) ☐ Responsive to communication(s) filed on ____.
- 2a) ☐ This action is **FINAL**. 2b) ☒ This action is non-final.
- 3) ☐ Since this application is in condition for allowance except for formal matters, prosecution as to the merits is closed in accordance with the practice under *Ex parte Quayle*, 1935 C.D. 11, 453 O.G. 213.

Disposition of Claims

- 4) ☒ Claim(s) 1-30 is/are pending in the application.
- 4a) Of the above claim(s) ____ is/are withdrawn from consideration.
- 5) ☐ Claim(s) ____ is/are allowed.
- 6) ☒ Claim(s) 1-30 is/are rejected.
- 7) ☐ Claim(s) ____ is/are objected to.
- 8) ☐ Claim(s) ____ are subject to restriction and/or election requirement.

Application Papers

- 9) ☐ The specification is objected to by the Examiner.
- 10) ☒ The drawing(s) filed on 21 October 2003 is/are: a) ☒ accepted or b) ☐ objected to by the Examiner.
Applicant may not request that any objection to the drawing(s) be held in abeyance. See 37 CFR 1.85(a).
Replacement drawing sheet(s) including the correction is required if the drawing(s) is objected to. See 37 CFR 1.121(d).
- 11) ☐ The oath or declaration is objected to by the Examiner. Note the attached Office Action or form PTO-152.

Priority under 35 U.S.C. § 119

- 12) ☐ Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).
- a) ☐ All b) ☐ Some * c) ☐ None of:
1. ☐ Certified copies of the priority documents have been received.
 2. ☐ Certified copies of the priority documents have been received in Application No. ____.
 3. ☐ Copies of the certified copies of the priority documents have been received in this National Stage application from the International Bureau (PCT Rule 17.2(a)).

* See the attached detailed Office action for a list of the certified copies not received.

Attachment(s)

- | | |
|---|---|
| 1) <input checked="" type="checkbox"/> Notice of References Cited (PTO-892) | 4) <input type="checkbox"/> Interview Summary (PTO-413)
Paper No(s)/Mail Date. ____. |
| 2) <input type="checkbox"/> Notice of Draftsperson's Patent Drawing Review (PTO-948) | 5) <input type="checkbox"/> Notice of Informal Patent Application (PTO-152) |
| 3) <input checked="" type="checkbox"/> Information Disclosure Statement(s) (PTO-1449 or PTO/SB/08)
Paper No(s)/Mail Date ____. | 6) <input type="checkbox"/> Other: ____. |

DETAILED ACTION

Claim Rejections - 35 USC 102

1. The following is a quotation of the appropriate paragraphs of 35 U.S.C. 102 that form the basis for the rejections under this section made in this Office action:

*A person shall be entitled to a patent unless --
(b) the invention was patented or described in a printed publication in this or a foreign country or in public use or on sale in this country, more than one year prior to the date of application for patent in the United States.*

2. Claims 1-30 are rejected under 35 U.S.C. 102(b) as being anticipated by Younis et al. (U.S. Patent No. 6,134,430).

Regarding claim 1, Younis discloses a programmable dynamic range receiver comprising a jammer detector for detecting the presence of jamming in an RF signal, and a state machine controlling the receiver based on the results of the jammer detector detecting the presence of jamming in the RF signal (refer to col. 3/lines 9-33 for the background on how jammers are generated and detected and how to minimize the degradation caused by jammers; Fig. 2, and col. 11/lines 20-48 & col. 12/lines 13-39 for the detection of jammers in an RF signal and the controlling or suppressing jammers, with AGC control circuit 1260 for interfacing to Demod 1250 or regarding as state machine within CDMA dedodulation).

As for claims 2-6, Younis discloses the programmable dynamic range receiver further comprising an amplifier, an RF bandpass filter (BPF), a mixer, an oscillator, a low pass filter (LPF), a comparator, a multi-bit ADC, and an attenuator with corresponding features of each components (Fig. 2, and Fig. 4 as a close-up view of item 1250 comprising a BPF 1410 and LPF 1416a & 1416b, and col. 6/line 54 to col. 8/line 19).

As for claims 7-8, Younis discloses these features of means for interfacing the state machine with the jammer detector (with AGC control circuit 1260 for interfacing to Demod 1250 or regarding as state machine within CDMA dedodulation); and utilizes a FFT calculation to determine the jamming in an RF signal (refer to the background as the spurious signals or jammers are determined, see col. 1/line 12 to col. 2/line 52 & col. 11/lines 20-29 for jammers within CDMA receivers).

As for claims 9-10, Younis further discloses these features of a power control including an up bit counter to adjust the gain in the receiver and the reporting step to the state machine as well as a timer for providing temporal hysteresis between the high and low power consumption modes based on the calculation or combination of bits counting in a period of time or within the presence of jamming in the RF signal (col. 4/lines 51-64 & col. 11/lines 20-48; and col. 17/line 35 to col. 18/line 51 for up bit counters).

As for claims 11-12, Younis further discloses the receiver further comprising an analog to digital converter functions to fewer bits based on the predetermined threshold (col. 15/lines 23-52 & col. 16/lines 12-21) and the state machine comprises logic circuitry (Fig. 4 and col. 9/lines 34-64).

As for claims 13-14, Younis further discloses the receiver further comprising wherein the state machine controls the receiver by software, hardware, or a combination of software and hardware, and further controls the receiver by adjusting, switching, parsing, biasing, turning on or off, or otherwise manipulating components of the receiver or system (refer again to the receiver architecture, see col. 6/line 43 to col. 8/line 63 with the control of AGC circuit 1260 for state machine or DEMOD 1250 as mentioned earlier).

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As for claim 15, refer to claim 9 above for the disclosure of Younis on the power control component.

As for claims 16-26, these claims with limitations addressed earlier are rejected for the reasons given in the scope of claims 1-14 as disclosed in details above.

As for claims 27-30, Younis discloses these features as the elements addressed earlier are integrated in an integrated chip (IC) or RF chip (Figs. 2 & 3 for the receiver is integrated within a receiving module or RF chip within the user device)

Conclusion

3. The prior art made of record and not relied upon is considered pertinent to applicant's disclosure:

Ciccarelli et al. and Barrick et al. (in PTO 892 attached) disclose systems related to programmable dynamic range receiver and method.

4. **Any response to this action should be mailed to:**

Commissioner of Patents and Trademarks

Washington, D.C. 20231

or faxed to the New Central Fax number:

(571) 273-8300, (for Technology Center 2600 only)

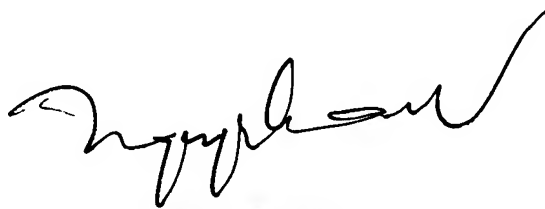
Hand deliveries must be made to Customer Service Window,

Randolph Building, 401 Dulany Street, Alexandria, VA 22314.

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5. Any inquiry concerning this communication or earlier communications from the examiner should be directed to Tony Thuan Nguyen whose telephone number is (571) 272-7895. The examiner can normally be reached on Monday-Friday from 9:30 AM to 7:00 PM, with alternate Fridays off.

Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see <http://pair-direct.uspto.gov>. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).



TONY T. NGUYEN
PATENT EXAMINER

Tony T. Nguyen
Art Unit 2685
October 27, 2005